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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE (USPTO)**

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<b>Filing Date</b>	August 10, 2001
<b>Title of Application</b>	Delay Circuit and Method
<b>First Named Inventor</b>	William A. Phillips
<b>Assignee</b>	ST Microelectronics, Inc. (large entity)
<b>Group Art Unit</b>	2816
<b>Examiner</b>	Kenneth Wells
<b>Attorney Docket Number</b>	1768-041
<b>Nature of the Office Communication to which this is responding</b>	Final Office Action
<b>Date of the Office Communication</b>	October 29, 2008
<b>Nature of this Document</b>	Supplemental Response to Final Office Action

**CERTIFICATE OF MAILING OR TRANSMISSION**

I hereby certify that this correspondence is being transmitted via the Office electronic filing system, EFS-Web, addressed to: MS AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 9<sup>th</sup> day of March 2009.

/Paola Kuvac/

Signature